

FIG.32

Conventional Art

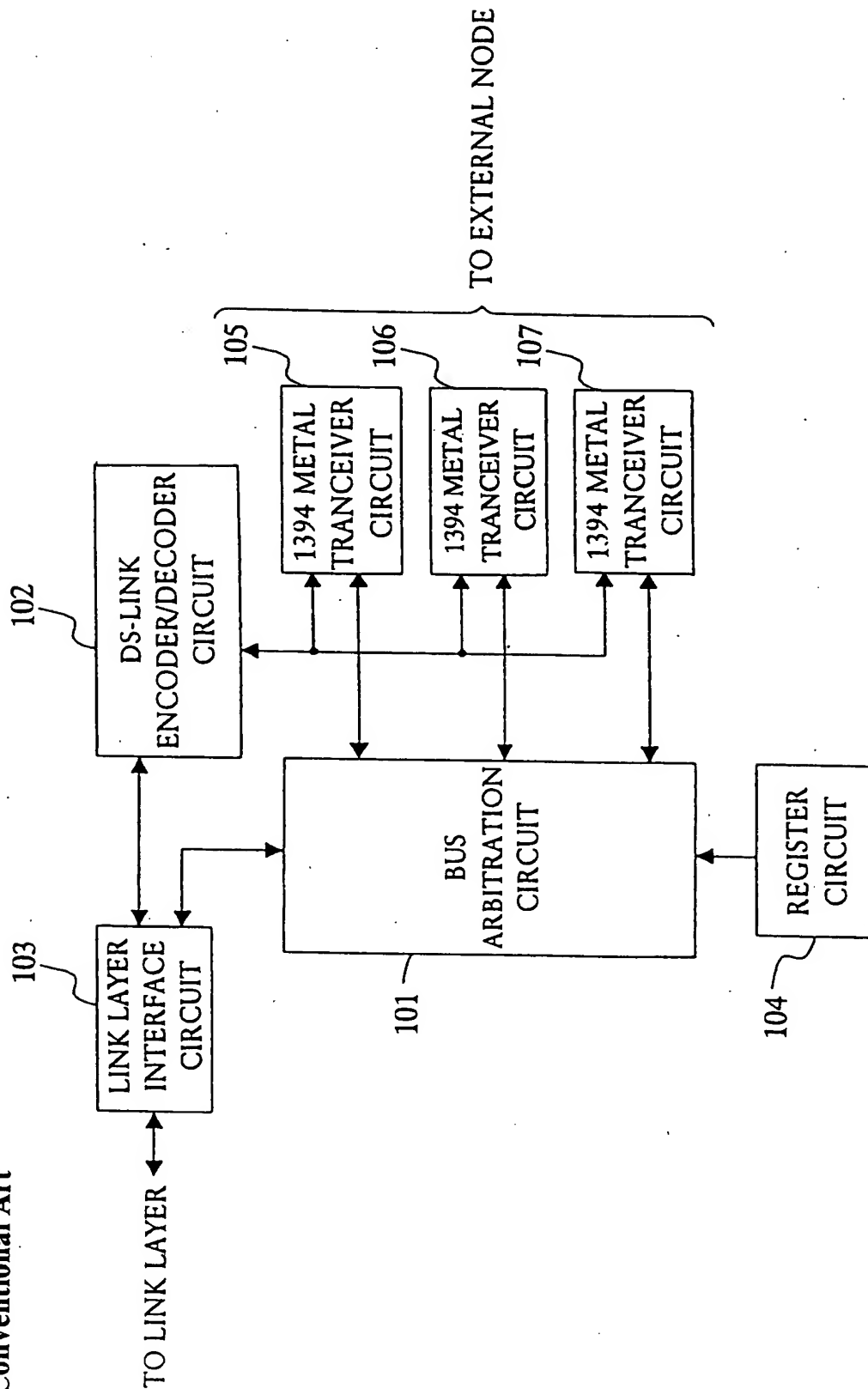


FIG. 33

Conventional Art

ADDRESS	0	1	2	3	4	5	6	7
0000	Physical_ID							
0001	RHB	IBR	Gap_count			R	PS	
0010	Extended(7)		Total_ports					
0011	Max_speed		Delay					
0100	LCtrl	Contender	Jitter		Pwr_class			
0101	Watchdog	ISBR	Loop	Pwr_fail	Timeout	Port_event	Enab_accel	Enab_multi
0110								
0111	Page_select		Port_select					
1000	Register0(page_select)							
1001								
1010								
1111	Register7(page_select)							

FIG.35

Conventional Art

		CONTENTS							
ADDRESS	0	1	2	3	4	5	6	7	
1000	4F								
1001	50								
1010	OP i.LINK version								
1011	Delay OP-DS			Jitter OP-DS				Pinging	
1100	Delay DS-DS			Jitter DS-DS					
1101	T0	T1	T2	T3	T4	T5	T6	T7	
1110	T8	T9	T10	T11	T12	T13	T14	T15	
1111	Ping timer								

FIG. 36

Conventional Art

10	Phy_ID	0	L	Gap_count	sp	rsv	c	pwr	p0	p1	p2	i	m
Logical inverse of first quadlet													
SELF ID PACKET #0													

10	Phy_ID	1	n(0)	nsv	p3	p4	p5	p6	p7	p8	p9	p1-	r	m
Logical inverse of first quadlet														
SELF ID PACKET #1														

10	Phy_ID	1	n(1)	nsv	p11	p12	p13	p14	p15	reserved
Logical inverse of first quadlet										
SELF ID PACKET #2										